Abstract

A semiconductor memory device having a bank for storing a data and a port as a data I/O terminal includes a global data bus for flowing an appearing current corresponding to the a data; a plurality of first transceivers, in response to the inputted instruction, for delivering the data between a bank to the global data bus; a plurality of first switching blocks, each for selectively connecting the global data bus to each of the plurality of first transceivers; a plurality of second transceivers, in response to the inputted instruction, for delivering the data between a port and the global data bus; and a plurality of second switching blocks, each for selectively connecting the global data bus to each of the plurality of the second transceivers.